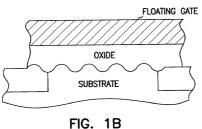
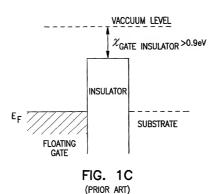


FIG. 1A (PRIOR ART)



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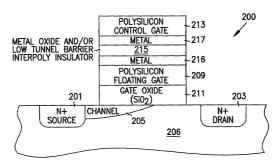
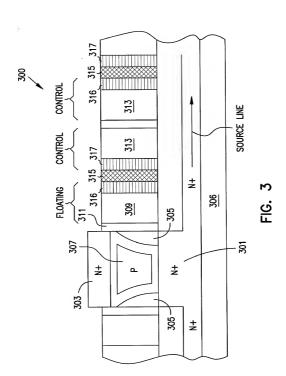
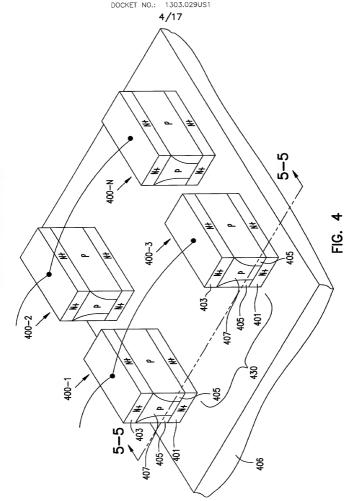


FIG. 2



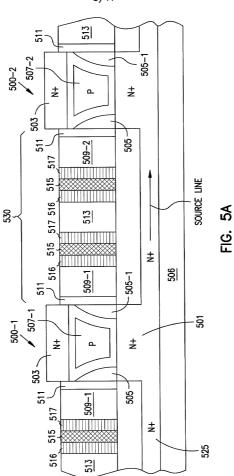
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TITLE: PROGRAMMABLE MEMORY ADDRESS AND DECODE CIRCUITS WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS
INVENTORS NAME: Leonard Forbes
DOCKET NO.: 1303.029US1



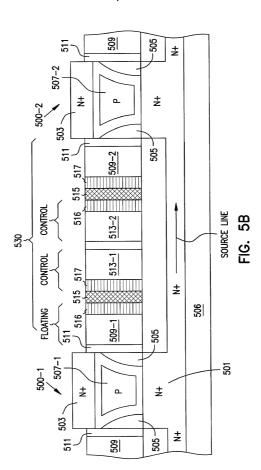
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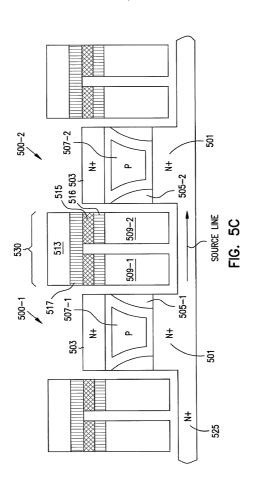


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INVENTORS NAME: Leonard Forbes DOCKET NO.: 1303.029US1

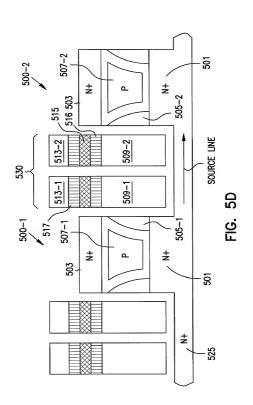


TITLE: PROGRAMMABLE MEMORY ADDRESS AND DECODE CIRCUITS WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS
INVENTORS NAME: Leonard Forbes
DOCKET NO.: 1303.029US1

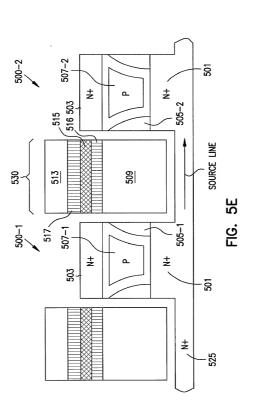


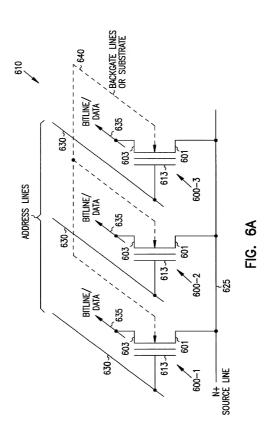
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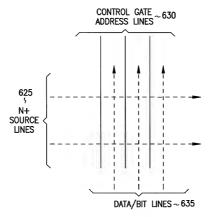
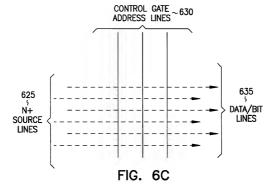
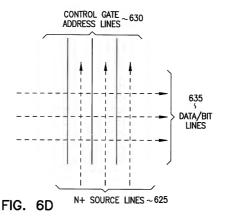
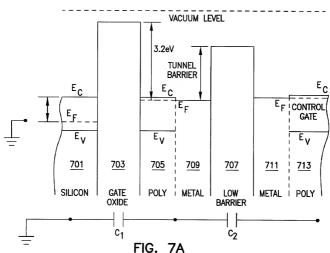


FIG. 6B







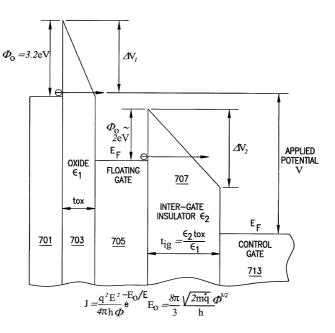


FIG. 7B

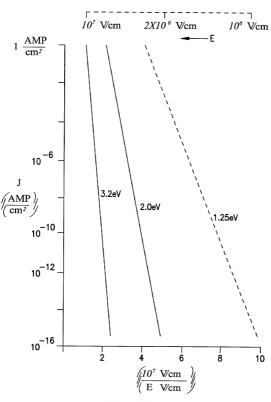


FIG. 7C

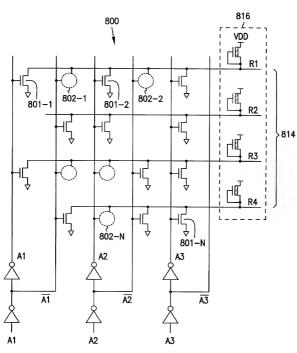


FIG. 8

BARRIER INTERPOLY INSULATORS
INVENTORS NAME: Leonard Forbes
DOCKET NO.: 1303.029US1

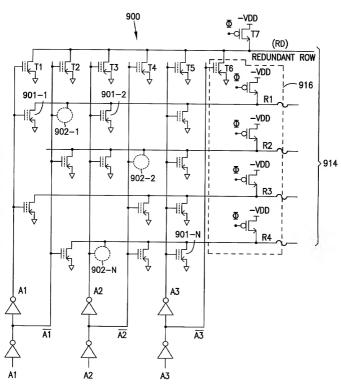


FIG. 9

TITLE: PROGRAMMABLE MEMORY ADDRESS AND DECODE CIRCUITS WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS
INVENTORS NAME: Leonard Forbes
DOCKET NO.: 1303.029US1

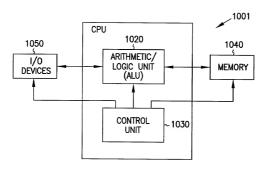


FIG. 10